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AGB75LC04-XX-X – Electrical and Environmental Specifications:

208 pin: AGB75LC04-QU-E / 225 pin: AGB75LC04-BG-E

Recommended Operating Conditions	
Power supply V_{cc}	3.0V to 3.6V, 3.3V nominal
Power supply V_{core}	1.08V to 1.32V, 1.2V nominal
Environmental Specifications	
Operating temperature	-40°C to +85°C
Storage temperature	-60°C to +150°C
DC Specifications	
V_{core} Supply current	22mA @ 1.2V
RoHS Compliant Package Options	
208-PQFP	28x28x3.4mm, 0.5 mm pin pitch
225-ball LFBGA	13x13x1.4mm, 0.8 mm ball pitch

The advanced power management controller has a very slow clock operating mode and software programmable power optimization capabilities. Along with a reset controller and shutdown controller, this gives the AGB75LC04-XX-X several low-power options.

The accelerated graphic memory architecture allows for concurrent access to 3 individual memory subsystems for simultaneous program execution, LCD refresh, and frame buffer rendering.

System Parameters:

LCDs supported	Up to 12” active (TFT) or passive and 24-bit color
Interfaces supported	USB, UART, TWI and SPI
Number of GPIO	39 Dedicated pins on the LFBGA and up to 17 on the PQFP package
Integrated Touch Panel Decoder	4- or 5-wire, calibration, noise filtering
SDRAM	32-bit parallel interface
Flash	AT45 Series Data Flash or Compatible Serial Data Flash Recommended